Approved for use through 07/31/2006. OMB 0651-0031 U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number. Application Number 10/734,627 TRANSMITTAL Filing Date 12/12/2003 First Named Inventor **FORM** J. Bharadwaj Art Unit 2186 Examiner Name Not yet assigned (to be used for all correspondence after initial filing) Attorney Docket Number P17845 Total Number of Pages in This Submission **ENCLOSURES** (Check all that apply) After Allowance Communication to TC Fee Transmittal Form Drawing(s) Appeal Communication to Board Licensing-related Papers Fee Attached of Appeals and Interferences Appeal Communication to TC Petition Amendment/Reply (Appeal Notice, Brief, Reply Brief) Petition to Convert to a After Final Provisional Application Proprietary Information Power of Attorney, Revocation Status Letter Change of Correspondence Address Affidavits/declaration(s) Other Enclosure(s) (please Identify Terminal Disclaimer Extension of Time Request below): - Copies of six (6) cited references Request for Refund Express Abandonment Request CD, Number of CD(s) Information Disclosure Statement Landscape Table on CD Certified Copy of Priority Remarks Document(s) Reply to Missing Parts/ Incomplete Application Reply to Missing Parts under 37 CFR 1.52 or 1.53 SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT Firm Name Intel Corporation Signature Printed name Justin B. Scout Date Reg. No. November 15, 2005 54,431 CERTIFICATE OF TRANSMISSION/MAILING I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below: Signature

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office. U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Derek S. Watson

Typed or printed name

Date

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Fees pursual to the Consolidated Appropriations Act, 2005 (H.R. 4818).			818).	Application Numb		10/734,627	
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For FY 2005				First Named Inve	entor	J. Bharadwai	
				Examiner Name		Not yet assigned	
Applicant claims sma	III entity status	. See 37 CFR 1.27		Art Unit		2186	
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Reissue	300	150	500	250	60	300	
Provisional	200	100	0	0	4	0 0	
2. EXCESS CLAIM FEES Fee Description Each claim over 20 (including Reissues) Each independent claim over 3 (including Reissues) Each independent claims Fee (\$) Fee (\$) Each independent claim over 3 (including Reissues) Multiple dependent claims Total Claims Fee (\$) Fee Paid (\$) HP = highest number of total claims paid for, if greater than 20. Indep. Claims Extra Claims Fee (\$) Fee Paid (\$) HP = highest number of independent claims paid for, if greater than 3. 3. APPLICATION SIZE FEE If the specification and drawings exceed 100 sheets of paper (excluding electronically filed sequence or computer listings under 37 CFR 1.52(e)), the application size fee due is \$250 (\$125 for small entity) for each additional 50 sheets or fraction thereof. See 35 U.S.C. 41(a)(1)(G) and 37 CFR 1.16(s). Total Sheets Extra Sheets Number of each additional 50 or fraction thereof Fee (\$) Fee Paid (\$) Fees Paid (\$) Fees Paid (\$)							
	Non-English Specification, \$130 fee (no small entity discount) Other (e.g., late filing surcharge):						
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SUBMITTED BY		11		
Signature	HR	JF	Registration No. (Attorney/Agent) 54,431	Telephone 503-264-7002
Name (Print/Type	Justin B.	Scout		Date November 15, 2005

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

J. Bharadwaj, et al.

Application No.: 10/734,627

Filed: December 12, 2003

For: METHOD FOR REGISTER ALLOCATION DURING

INSTRUCTION SCHEDULING

INCOMPENDED CENTS

Art Group: 2186

Examiner: Not yet assigned

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In accordance with the duty to disclosure, included herewith is form PTO/SB/08 or PTO-1449 indicating references cited and copies of the cited art except for copies not required to be submitted (e.g., copies of US patents and US published patent applications need not be disclosed for applications filed after June 30, 2003). This IDS and IDS Citation Form are being submitted before the mailing of a first Office Action. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

Pursuant to 37 C.F.R. § 1.97 the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any additional fee due to Deposit Account 50-0221. A duplicate copy of the Fee Transmittal is enclosed for this purpose.

Respectfully submitted, Intel Corporation

inter Corporation

Justin B. Scout, Reg. No. 54,431

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MADEMARK OF	Application Number	10/734,627		
INFORMATION DISCLOSURE	Filing Date	12/12/2003		
STATEMENT BY APPLICANT	First Named Inventor	J. Bharadwaj		
(Use as many sheets as necessary)	Art Unit	2186		
(Ose as many sneets as necessary)	Examiner Name	Not yet assigned		
Sheet 1 of 1	Attorney Docket Number	P17845		

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	1	S. Pinter, "Register Allocation with Instruction Scheduling: a New Approach", Dept. of Electrical Engineering, Israel Institute of Technology, Haifa, Israel,19 pages.	
	2	D. Bradlee et al., "Integrating Register Allocation and Instruction Scheduling for RISCs", Dept. of Computer Science, Univ. of Washington, Seattle, Washington, 1991, 9 pages.	
	3	G. Chaitin, "Register Allocation & Spilling Via Graph Coloring", IBM Research, Yorktown Heights, NY, 1982, 4 pages.	
	4	M. Poletto et al., "Linear Scan Register Alocation" ACM Trans. on Programming Languages and Systems, Vol. 21, No. 5, Sept. 1999, Pages 895-913.	
	5	S. Freudenberger et al., "Phase Ordering of Register Allocation and Instruction Scheduling", Presented at Code Generation - Concepts, Tools & Techniques, July 1991, 10 pages.	
	6	F. Chow et al., "The Priority-Based Coloring Approach to Register Allocation", ACM Trans. on Programming Languages and Systems, Vol. 12, No 4, October 1990, pages 501-536.	

Examiner	Date	
Signature	Consi	dered

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

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